

A RECONNECTABLE CHIP INTERFACE AND CHIP PACKAGEAbstract of the Invention

An assembly of the present invention has a substrate and an integrated circuit device adapted to be electrically and mechanically connected to the substrate. Electrical connection pads on the circuit device and on the substrate contact one another when the circuit device and the substrate are connected. At least one first projection on one of the device and on the substrate and at least two second projections on the other of the device and the substrate each have a respective axial length and are sized and shaped for a close friction fit along their axial lengths when the projections are interdigitated relative to one another. An integrated circuit device package of the present invention includes an integrated circuit device and an interconnect substrate for mounting the circuit device on an electronic circuit substrate. The interconnect substrate mates with the active side of the circuit device to form an enclosed space.